

## ABSTRACT

A demodulation method for establishing clock synchronization within a short period of time from a received signal modulated by  $\pi/4$ -shift QPSK modulation that contains a synchronization establishment signal wherein the change in phase periodically alternates between positive and negative, and for demodulating the received signal. The method includes establishing synchronization from the received signal based on the timing of changes in the positive/negative polarity of the change in phase of the synchronization establishment signal contained in the received signal, and demodulating the received signal.